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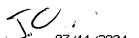
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16:17 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE In re Application of: § Jeff Brown et. al. Group Art Unit: 10/719,878 Serial No. : Examiner: Filed: November 21, 2003 Atty Docket: 5201-27900 / 03-Device And Method For Using A For: 0940 Lessened Load To Measure Signal Skew At The Output Of An I hereby certify that this correspondence is being deposited Integrated Circui with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on the date below: REQUEST FOR CORRECTED FILING RECEIPT Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450 Dear Sir: Attached is a copy of the Official Filing Receipt received from the U.S. Patent and Trademark Office in connection with the above application for which issuance of a corrected Filing Receipt is respectfully requested. There is an arror with respect to the following data which was: incorrectly entered omitted. and/or ERROR IN **CORRECT DATA** Applicant(s) name Jeff Brown Applicant(s) address Title of Application Device And Method For Using A Lessened Load To Measure Signal Skew At The Output X Filing Date 11/21/03 Serial Number 10/719,878 Foreign/PCT Applcn. Re: Other: The correction(s) is/are not due to any error by applicant and no fee is due. At least one of the above correction(s) is/are due to applicant(s) error and the Commissioner is hereby authorized to charge the fee of \$25.00 under 37 CFR 1.19(h) to Deposit Account 12-2252. LSI Logic Corporation Respectfully submitted, 1551 McCarthy Blvd., MS D-106 Milipitas, CA 95035

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

5201-27900 Atty. Dkt. No. 03-0940 § § § Inventor(s): @ @ @ @ @ @ @ @ @ Jeffrey S. Brown Craig R. Chafin Title: DEVICE AND METHOD FOR USING A LESSENED LOAD TO MEASURE

SIGNAL SKEW AT THE OUTPUT OF AN INTEGRATED CIRCUIT

CERTIFICATE OF EXPRESS MAIL UNDER 37 C.F.R. § 1.10 Express Mail" mailing label no. EV403685447US Date of Deposit: November 21, 2003 I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to

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UTILITY PATENT APPLICATION TRANSMITTAL (For new non-provisional applications under 37 CFR § 1.53(b))

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Enclosed are the following items:

The Commissioner is hereby authorized to charge the filing fee to LSI Logic Corporation deposit 1. account no. 12-2252/03-0940. A duplicate of this page is enclosed. The filing fee is calculated as shown below.

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- Informal drawings of Figs. 1-5 on 3 sheets. 4.
- 5. Newly executed Declaration/Power of Attorney.
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Attorney for Applicants

Conley Rose, P.C. P.O. Box 684908 Austin, TX 78768-4908 Date: November 21, 2003

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PATENT 5201-27900 03-0940

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DEVICE AND METHOD FOR USING A LESSENED LOAD TO MEASURE SIGNAL SKEW AT THE OUTPUT OF AN INTEGRATED CIRCUIT

By:

Jeffrey S. Brown 3624 Goodell Lane Ft. Collins, Colorado 80528 Citizenship: USA

Craig R. Chaffu 627 Paradise Lane Colorado Springs, Colorado 80904 Citizenship: USA

Agy. Dkt. No. \$201-27900

Kevin L. Daffer Conley Rose, P.C. P.O. Box 684908 Austin, TX 78768-4908 Pb: (512) 476-1400



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Leo Peters
LSI Logic Corporation
1621 Barber Lane, MS D-106
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Applicant(s)

Jeffrey S. Brown, Ft. Collins, CO; Craig R. Chafin, Colorado Springs, CO;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 02/20/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Device and method for using a lessened load to measure signal skew at the output of an integrated circuit

Preliminary Class

324

16:17

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